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L1	10	(Janzen near Leel).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:17
L2	26414	"711"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:17
L3	68559	"365"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:19
L4	1548	(self adj refresh) near2 mode$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:20
L5	8731	(low adj power) near2 mode$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:20
L6	700	(refresh) adj2 (voltage or level)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:22
L7	5281	refresh adj2 (rate or frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:26
L8	92859	2 or 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:26
L9	268	4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:26
L10	165	8 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:26

L11	46	6 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:28
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L13	27024	(input adj2 voltage) same (var$4 or adjust$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:36
L14	2	12 and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:36
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L16	4492684	(constant or fixed or static)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:37
L17	2045941	15 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:38
L18	18	17 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:38
L19	2	18 and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 13:38

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1 Power-aware approach for microprocessor design: LPRAM: a low power DRAM with testability

Subhasis Bhattacharjee, Dhiraj K. Pradhan

January 2004 **Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04 , Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04**

Full text available: pdf(137.94 KB) Publisher Site

Additional Information: full citation, abstract, references

To date all the proposal for low power designs of RAMs essentially focus on circuit level solutions. What we propose here is a novel architecture level solution. Our methodology provides a systematic trade off between power and area. Also, it allows tradeoff between test time and power consumed in test mode. Significantly, too, the proposed design has the potential to achieve performance improvements while reducing power. In this respect it stands apart from other approaches where the convention ...

2 Energy conservation for mobile devices: Sleep: a technique for reducing energy consumption in handheld devices

Lawrence S. Brakmo, Deborah A. Wallach, Marc A. Viredaz

June 2004 **Proceedings of the 2nd international conference on Mobile systems, applications, and services MobiSys '04**

Full text available: pdf(227.79 KB) Additional Information: full citation, abstract, references, index terms

Energy management has become one of the great challenges in portable computing. This is the result of the increasing energy requirements of modern portable devices without a corresponding increase in battery technology. *uSleep* is an energy reduction technique for handheld devices that is most effective when the handheld's processor is lightly loaded, such as when the user is reading a document or looking at a web page. When possible, rather than using the processor's idle mode, uSleep tri ...

Keywords: energy management, power management, processor sleep

3 Wireless application drivers for low-power systems: Experience with a low power wireless mobile computing platform

Vijay Raghunathan, Trevor Pering, Roy Want, Alex Nguyen, Peter Jensen

August 2004 **Proceedings of the 2004 international symposium on Low power electronics and design**

Full text available: pdf(315.45 KB) Additional Information: full citation, abstract, references, index terms

A detailed power analysis of a multi-radio mobile platform highlights the complex tradeoffs between the computation, storage, and communication subsystems. The particular mobile device, which does not include an LCD or other on-board display, can be used as a source for audio or video media files, or a source/sink for secure data transfers. A version of the device has been augmented with fine-grained power monitoring capability and used to obtain detailed measurements of power dissipation in the ...

Keywords: mobile computing, power management, wireless systems

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